

**AMENDMENTS TO THE SPECIFICATION**

**Please replace the paragraph in the specification beginning on page 17, line 16, paragraph [0052], with the following rewritten paragraph:**

The CA register 121 is also connected to the DRAMs 115 in the ~~subless~~ stubless configuration and is terminated by the termination resistor 120 at the end. The GND layer is shorted and the power supply and the GND layer are connected by a bypass capacitor so that the return current flowing through the power-supply/GND layer is not cut in pieces at a point near the bus line loop-back point.